REMARKS

Claims 1-38 are pending in the application. Claims 1-38 were rejected under 35 U.S.C. §103(a) as being unpatentable over Chawla et al. in view of Chen et al., and Huang et al.

Claims 1, 15 and 29 have been amended. Reconsideration and reexamination of the application in view of the amendments and following remarks is respectfully requested.

Claims 1-38 were rejected under 35 U.S.C. §103(a) as being unpatentable over Chawla in view of Chen, and Huang. With the amendment to claims 1, 15 and 29, it is respectfully submitted that this rejection has been overcome.

Claims 1, 15 and 29 have been amended to recite the additional limitation "issuing an identifier command to the nonvolatile memory" and "receiving from the nonvolatile memory an identifier that *identifies* a memory type of the nonvolatile memory to determine appropriate commands to be sent." (Emphasis added). In particular, the nonvolatile memory returns an identifier that specifically identifies a memory type.

As acknowledged by the Examiner, Chawla "does not expressly teach identifying a memory type of the nonvolatile memory to determine appropriate commands to be sent." (Office Action, page 3). Furthermore, Chen contains no disclosure at all related to identifying a memory type of the nonvolatile memory. Therefore, Chawla and Chen both fail to disclose, teach, or suggest receiving from nonvolatile memory an identifier that *identifies* a memory type of the nonvolatile memory to determine appropriate commands to be sent.

Further, Huang fails to make up for the deficiencies of Chawla and Chen. Instead, Huang is seen to disclose a "process unit [that] determines which type of flash ROM is disposed ... according to the signal output from the strapping component after receiving a *memory access request with an access range*." (Huang, column 2, lines 38-41 (Emphasis added)). In particular, the signal output of Huang is either: (1) a low pin count memory instruction; or (2) a firmware hub memory instruction. (Huang, column 2, lines 41-49). As a result, unlike the claimed invention, Huang is limited to distinguishing between a low pin count flash read only memory (ROM) and a

firmware hub flash ROM based on the signal output. Huang does not disclose issuing an identifier command to the nonvolatile memory <u>instead</u> of a memory access request.

Because neither Chawla nor Chen nor Huang, alone or in combination, discloses, teaches, or suggests all of the limitations of amended claims 1, 15 and 29, and because claims 2-14 depend from amended claim 1, claims 16-28 depend from amended claim 15, and claims 30-38 depend from amended claim 29, it is respectfully submitted that the rejection of claims 1-38 under 35 U.S.C. §103(a) as being unpatentable over Chawla in view of Chen and Huang has been overcome.

In view of the above, each of the presently pending claims in this application is believed to be in immediate condition for allowance. Accordingly, the Examiner is respectfully requested to pass this application to issue.

If, for any reason, the Examiner finds the application other than in condition for allowance, Applicants request that the Examiner contact the undersigned attorney at the Los Angeles telephone number (213) 892-5752 to discuss any steps necessary to place the application in condition for allowance.

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In the unlikely event that the transmittal letter is separated from this document and the Patent Office determines that an extension and/or other relief is required, Applicants petition for any required relief including extensions of time and authorizes the Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to **Deposit Account No. 03-1952** referencing Docket No. 491442002000.

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Respectfully submitted,

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